

Amendments to the Claims

The listing of claims will replace all prior versions, and listings of claims in the application.

1. (Currently Amended) A serial data interface system comprising:

a first transceiver configured to comply with a first standard coupled to a set of pins of an interface wherein said first standard is a data-strobe standard; and

a second transceiver configured to comply with a second standard coupled to the set of pins wherein said second standard is a serializer-deserializer standard, wherein said second transceiver comprises a receiver section further comprising a clock recovery system, a deserializer, a comma detect and alignment device, and a signal detect device, wherein the interface can transmit and receive a signal and can electronically change between the first and the second standard depending on the signal being transmitted or received.

2. (Original) The system of claim 1, wherein:

the first standard is IEEE 1394-1995/1394a-2000 standard; and
the second standard is IEEE 1394b-2002 standard.

3. (Previously Presented) The system of claim 1, wherein the first transceiver device comprises:

a twisted-wire pair (TP) bias section;
a first TP transceiver section; and
a second TP transceiver section.

4. (Previously Presented) The system of claim 3, wherein the TP bias section comprises:

a TP bias device; and
a connection detection device.

5. (Original) The system of claim 3, wherein the first TP transceiver section comprises:

- a strobe signal device;
- a data signal device;
- an arbitration signal device; and
- a speed detection device.

6. (Original) The system of claim 5, wherein at least one of the strobe signal, data signal, arbitration signal, and speed detection devices is an asynchronous device.

7. (Original) The system of claim 3, wherein the second TP transceiver section comprises:

- a strobe signal device;
- a data signal device;
- an arbitration signal device; and
- a bias signal detection device.

8. (Original) The system of claim 7, wherein at least one of the strobe signal, data signal, arbitration signal, and bias signal detection devices is an asynchronous device.

9. (Currently Amended) The system of claim 1, wherein the second transceiver further comprises:

- a transmitter section ~~coupled to the second pin; and~~
- ~~_____ a receiver section coupled to the first pin.~~ wherein the transmitter section is coupled to a first pin in the set of pins and the receiver section is coupled to a second pin in the set of pins.

10. (Original) The system of claim 9, wherein the transmitter section comprises:

- a clock;
- a serializer; and

a driver.

11. (Previously Presented) The system of claim 10, wherein the serializer comprises a N to 1 serializer, wherein N is an integer equal or larger than 2.

12. (Cancelled)

13. (Currently Amended) The system of claim ~~12~~ 1, wherein the deserializer comprises a 1-to-N deserializer, wherein N is an integer number equal to or larger than 2.

14. (Currently Amended) The system of claim ~~12~~ 1, wherein the clock recovery system comprises:

- a phase detector;
- a loop filter; and
- a phase interpolator.

15. (Currently Amended) A serial data interface system, comprising a single port comprising:

- a first section configured to comply with a first standard wherein said first standard is a data-strobe standard including,

- a TPBIAS device section coupled to first and second pins (through additional circuitry),

- a first transceiver section coupled to the first and second pins, and

- a second transceiver section coupled to third and fourth pins, and

- a second section configured to comply with a second standard wherein said second standard is a serializer-deserializer standard including,

- a signal transmitting device coupled to the third and fourth pins, and

- a signal receiving device coupled to the first and second pins

wherein the receiving device further comprises a clock recovery system, a deserializer, a comma detect and alignment device, and a signal detect device;

wherein the interface can transmit and receive a signal and can electronically change between the first and the second standard depending on the signal being transmitted or received.

16. (Original) The system of claim 15, wherein:

the first standard is IEEE 1394-1995/1394a-2000 standard; and

the second standard is IEEE 1394b-2002 standard.

17. (Previously Cancelled)

18. (Currently Amended) A method comprising:

(a) transmitting and receiving data in compliance with a first standard wherein said first standard is a data-strobe standard on first and second differential media pairs;

(b) transmitting data in compliance with a second standard wherein said second standard is a serializer-deserializer standard on the first differential media pair;

(c) receiving data in compliance with the second standard on the second differential media pair wherein the data received in compliance with the second standard is input to receiver section comprising a signal detect device, a deserializer, a comma detect and alignment device, and a clock recovery system; and

(d) switching use of the first and second differential media pair between step (a) and steps (b) and (c) depending on the data being transmitted or received.

19. (Original) The method of claim 18, wherein steps (b) and (c) are performed substantially simultaneously.

20. (Original) The method of claim of claim 18, further comprising:

using IEEE 1394-1995/1394a-2000 as the first standard; and

using IEEE 1394b-2002 as the second standard.

21. (Original) The system of claim 1, wherein the first transceiver comprises:

- a bias section;
- a first transceiver section; and
- a second transceiver section.

22. (Original) The system of claim 21, wherein the bias section comprises:

- a bias device; and
- a connection detection device.

23. (Original) The system of claim 21, wherein the second transceiver section comprises:

- a strobe signal device;
- a data signal device;
- an arbitration signal device; and
- a bias signal detection device.

24. (Original) The system of claim 23, wherein at least one of the strobe signal, data signal, arbitration signal, and bias signal detection devices is an asynchronous device.